

ESD (HUMAN BODY MODE) TEST REPORT

Company :	RAIO TECHNOLOGY INC.

Model Name : <u>RA8875</u>

Date Received : JAN 12, 2011

Date Tested : JAN 17, 2011

TESTING LABORATORY IS ACCREDITED BY:

IEC/IECQ 17025 certificate of independent test laboratory approval

Certificate No. : T1091

ISO 9001 certificate is approved by TUV CERT certification body of TUV NORD Cert GmbH

WE HEREBY CERTIFY THAT:

The test(s) shown in the attachment were conducted according to the indicating procedures. We assume full responsibility for the accuracy and completeness of these tests and vouch for the qualifications of all personnel performing them.

	Name	Signature	Date
Test Engineer	Nelly Hsieh	Nelly Hsiet	Jan 17, 2011
Manager	Even Lin	Tomte	Jan 17, 2011

Note :

- 1. This report will be invalid if reproduced in whole or in part.
- 2. This report refers only to the specimen(s) submitted to test, and is invalid if used separately.
- 3. This report is ONLY valid with the examination seal and signature of this institute.
- 4. The tested specimen(s) will only be preserved for thirty days from the date issued, if not collected by the applicant.
- 5. The failure criteria should be based on parametric and functiona basing; the curve trace provided in this report is for reference only. 報告專用章



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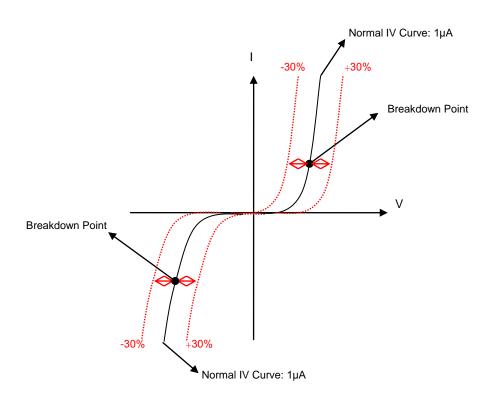
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1. GENERAL INFORMATION

1.1 DESCRIPTION OF UNIT

MANUFACTURER	: RAIO TECHNOLOGY INC.S
DEVICE NAME	: RA8875
PACKAGED / PIN COUNT	: LQFP-100
REFERENCE DOCUMENT	: MIL-STD-883G Method 3015.7
TEST VOLTAGE	: 2000V ~ 8000V (±), Step: 1000V (±)
SAMPLE QUANTITY	: 18 ea
FAILURE CRITERIA	:FOR V CHANGE AT 1μA ±30%

% Failure Judgment: IV curve shift over 1µA±30% at breakdown point.





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2. ESD (HUMAN BODY MODE) TEST

2.1 TEST EQUIPMENT

Test Equipment	Equipment Number	Tester		
KEYTEK ZAPMASTER	#11	02009		

2.2 LABORATORY AMBIENCE CONDITION

Temperature : 23±5°C Relative humidity : 55%±10% (RH)

2.3 REFERENCE DOCUMENT

The test method refers to MIL-STD-883G Method 3015.7

2.4 TEST CONDITION

ALL - VSS (+) ALL - VSS (-) ALL - VCC (+) ALL - VCC (-) VCC - VSS (+) VCC - VSS (-)

2.5 SUMMARY OF TEST

Test Model : HBM	ESD Sensitivity	Passed : <u>±5000V</u>	MIL-STD Classification Class : <u>3A</u>
Test condition	Sample Quantity	Passed Volts	Class 0 : < 250V.
ALL – VSS (+)	3	+5000V	Class 1A : ≧ 250V, <499V Class 1B : ≧ 500V, <999V
ALL – VSS (-)	3	-8000V	Class 1C : \geq 1000V, <1999V
ALL – VCC (+)	3	+7000V	Class 2 : \geq 2000V , <3999V
ALL – VCC (-)	3	-5000V	Class 3A : ≧ 4000V,<7999V Class 3B : ≧ 8000V
VCC – VSS (+)	3	+5000V	
VCC – VSS (-)	3	-8000V	

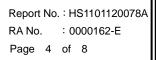
ALL:3-4,7-12,14-15,18-25,27-37,41-59,63-69,71-75,77 VSS:1,6,16,26,39,61,70,85

-84,88-100

VCC:2,5,13,17,38,40,60,62,76,86-87



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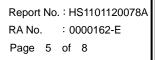


2.6 CONTENTS OF TEST

ALL – VSS (+)							(UNIT:V)
Test FAIL Pin VOLTAGE	#1	#2	#3	Test FAIL Pin VOLTAGE	#1	#2	#3
3	8000	8000	PASS	53	PASS	PASS	PASS
4	7000	8000	6000	54	PASS	PASS	PASS
7	PASS	PASS	PASS	55	PASS	PASS	PASS
8	PASS	PASS	PASS	56	PASS	PASS	PASS
9	PASS	PASS	PASS	57	PASS	PASS	PASS
10	PASS	PASS	PASS	58	PASS	PASS	PASS
11	PASS	PASS	PASS	59	PASS	PASS	PASS
12	PASS	PASS	PASS	63	PASS	PASS	PASS
14	PASS	PASS	PASS	64	PASS	PASS	PASS
15	PASS	PASS	PASS	65	PASS	PASS	PASS
18	PASS	PASS	PASS	66	PASS	PASS	PASS
19	PASS	PASS	PASS	67	PASS	PASS	PASS
20	PASS	PASS	PASS	68	PASS	PASS	PASS
21	PASS	PASS	PASS	69	PASS	PASS	PASS
22	PASS	PASS	PASS	71	PASS	PASS	PASS
23	PASS	PASS	PASS	72	PASS	PASS	PASS
24	PASS	PASS	PASS	73	PASS	PASS	PASS
25	PASS	PASS	PASS	74	PASS	PASS	PASS
27	PASS	PASS	PASS	75	PASS	PASS	PASS
28	PASS	PASS	PASS	77	PASS	PASS	PASS
29	PASS	PASS	PASS	78	PASS	PASS	PASS
30	PASS	PASS	PASS	79	PASS	PASS	PASS
31	PASS	PASS	PASS	80	PASS	PASS	PASS
32	PASS	PASS	PASS	81	PASS	PASS	PASS
33	PASS	PASS	PASS	82	PASS	PASS	PASS
34	PASS	PASS	PASS	83	PASS	PASS	PASS
35	PASS	PASS	PASS	84	PASS	PASS	PASS
36	PASS	PASS	PASS	88	PASS	PASS	PASS
37	PASS	PASS	PASS	89	PASS	PASS	PASS
41	PASS	PASS	PASS	90	PASS	PASS	PASS
42	PASS	PASS	PASS	91	PASS	PASS	PASS
43	PASS	PASS	PASS	92	PASS	PASS	PASS
44	PASS	PASS	PASS	93	PASS	PASS	PASS
45	PASS	PASS	PASS	94	PASS	PASS	PASS
46	PASS	PASS	PASS	95	PASS	PASS	PASS
47	PASS	PASS	PASS	96	PASS	PASS	PASS
48	PASS	PASS	PASS	97	PASS	PASS	PASS
49	PASS	PASS	PASS	98	PASS	PASS	PASS
50	PASS	PASS	PASS	99	PASS	PASS	PASS
51	PASS	PASS	PASS	100	PASS	PASS	PASS
52	PASS	PASS	PASS				



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			ALL –	VSS (-)			(UNIT:V)
Test FAIL Pin VOLTAGE	#1	#2	#3	Test FAIL Pin VOLTAGE	#1	#2	#3
3	PASS	PASS	PASS	53	PASS	PASS	PASS
4	PASS	PASS	PASS	54	PASS	PASS	PASS
7	PASS	PASS	PASS	55	PASS	PASS	PASS
8	PASS	PASS	PASS	56	PASS	PASS	PASS
9	PASS	PASS	PASS	57	PASS	PASS	PASS
10	PASS	PASS	PASS	58	PASS	PASS	PASS
11	PASS	PASS	PASS	59	PASS	PASS	PASS
12	PASS	PASS	PASS	63	PASS	PASS	PASS
14	PASS	PASS	PASS	64	PASS	PASS	PASS
15	PASS	PASS	PASS	65	PASS	PASS	PASS
18	PASS	PASS	PASS	66	PASS	PASS	PASS
19	PASS	PASS	PASS	67	PASS	PASS	PASS
20	PASS	PASS	PASS	68	PASS	PASS	PASS
21	PASS	PASS	PASS	69	PASS	PASS	PASS
22	PASS	PASS	PASS	71	PASS	PASS	PASS
23	PASS	PASS	PASS	72	PASS	PASS	PASS
24	PASS	PASS	PASS	73	PASS	PASS	PASS
25	PASS	PASS	PASS	74	PASS	PASS	PASS
27	PASS	PASS	PASS	75	PASS	PASS	PASS
28	PASS	PASS	PASS	77	PASS	PASS	PASS
29	PASS	PASS	PASS	78	PASS	PASS	PASS
30	PASS	PASS	PASS	79	PASS	PASS	PASS
31	PASS	PASS	PASS	80	PASS	PASS	PASS
32	PASS	PASS	PASS	81	PASS	PASS	PASS
33	PASS	PASS	PASS	82	PASS	PASS	PASS
34	PASS	PASS	PASS	83	PASS	PASS	PASS
35	PASS	PASS	PASS	84	PASS	PASS	PASS
36	PASS	PASS	PASS	88	PASS	PASS	PASS
37	PASS	PASS	PASS	89	PASS	PASS	PASS
41	PASS	PASS	PASS	90	PASS	PASS	PASS
42	PASS	PASS	PASS	91	PASS	PASS	PASS
43	PASS	PASS	PASS	92	PASS	PASS	PASS
44	PASS	PASS	PASS	93	PASS	PASS	PASS
45	PASS	PASS	PASS	94	PASS	PASS	PASS
46	PASS	PASS	PASS	95	PASS	PASS	PASS
47	PASS	PASS	PASS	96	PASS	PASS	PASS
48	PASS	PASS	PASS	97	PASS	PASS	PASS
49	PASS	PASS	PASS	98	PASS	PASS	PASS
50	PASS	PASS	PASS	99	PASS	PASS	PASS
51	PASS	PASS	PASS	100	PASS	PASS	PASS
52	PASS	PASS	PASS				

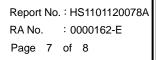


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ALL – VCC (+)							(UNIT:V)
Test FAIL Pin VOLTAGE	#1	#2	#3	Test FAIL Pin VOLTAGE	#1	#2	#3
3	PASS	PASS	PASS	53	8000	8000	8000
4	PASS	PASS	PASS	54	PASS	8000	8000
7	PASS	PASS	PASS	55	8000	8000	8000
8	PASS	PASS	PASS	56	PASS	8000	8000
9	PASS	PASS	PASS	57	PASS	PASS	8000
10	PASS	PASS	PASS	58	PASS	PASS	PASS
11	PASS	PASS	PASS	59	PASS	PASS	PASS
12	PASS	PASS	PASS	63	PASS	8000	8000
14	PASS	PASS	PASS	64	PASS	PASS	PASS
15	PASS	PASS	PASS	65	PASS	PASS	PASS
18	PASS	PASS	PASS	66	PASS	PASS	PASS
19	PASS	PASS	PASS	67	PASS	PASS	PASS
20	PASS	PASS	PASS	68	PASS	PASS	PASS
21	PASS	PASS	PASS	69	PASS	PASS	PASS
22	PASS	PASS	PASS	71	PASS	PASS	PASS
23	PASS	PASS	PASS	72	PASS	PASS	PASS
24	PASS	PASS	PASS	73	PASS	PASS	PASS
25	PASS	PASS	PASS	74	PASS	PASS	PASS
27	PASS	PASS	PASS	75	PASS	PASS	PASS
28	PASS	8000	PASS	77	PASS	PASS	PASS
29	8000	8000	PASS	78	PASS	PASS	PASS
30	8000	PASS	PASS	79	PASS	PASS	PASS
31	PASS	8000	PASS	80	PASS	PASS	PASS
32	8000	8000	PASS	81	PASS	PASS	PASS
33	PASS	8000	PASS	82	PASS	PASS	PASS
34	PASS	8000	PASS	83	PASS	PASS	PASS
35	8000	8000	PASS	84	PASS	PASS	PASS
36	8000	8000	PASS	88	PASS	PASS	PASS
37	8000	8000	PASS	89	PASS	PASS	PASS
41	PASS	PASS	PASS	90	PASS	PASS	PASS
42	PASS	PASS	PASS	91	PASS	PASS	PASS
43	PASS	PASS	PASS	92	PASS	PASS	PASS
44	PASS	PASS	PASS	93	PASS	PASS	PASS
45	8000	8000	8000	94	PASS	PASS	PASS
45	PASS	PASS	PASS	95	PASS	PASS	PASS
47	8000	8000	PASS	96	PASS	PASS	PASS
48	8000	8000	PASS	97	PASS	PASS	PASS
49	PASS	PASS	PASS	98	PASS	PASS	PASS
50	8000	8000	8000	99	PASS	PASS	PASS
51	8000	8000	8000	100	PASS	PASS	PASS
52	8000	8000	PASS				



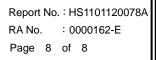
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			ALL –	VCC (-)			(UNIT:V)
Test FAIL Pin VOLTAGE	#1	#2	#3	Test FAIL Pin VOLTAGE	#1	#2	#3
3	PASS	PASS	PASS	53	PASS	PASS	PASS
4	PASS	PASS	PASS	54	PASS	PASS	PASS
7	PASS	PASS	PASS	55	PASS	PASS	PASS
8	PASS	PASS	PASS	56	PASS	PASS	PASS
9	PASS	PASS	PASS	57	PASS	PASS	PASS
10	PASS	PASS	PASS	58	PASS	PASS	PASS
11	PASS	PASS	PASS	59	PASS	PASS	PASS
12	PASS	PASS	PASS	63	PASS	PASS	PASS
14	PASS	PASS	PASS	64	PASS	PASS	PASS
15	PASS	PASS	PASS	65	PASS	PASS	PASS
18	PASS	PASS	PASS	66	PASS	PASS	PASS
19	PASS	PASS	PASS	67	PASS	PASS	PASS
20	PASS	PASS	PASS	68	PASS	PASS	PASS
21	PASS	PASS	PASS	69	PASS	PASS	PASS
22	PASS	PASS	PASS	71	-6000	-8000	PASS
23	PASS	PASS	PASS	72	PASS	PASS	PASS
24	PASS	PASS	PASS	73	PASS	PASS	PASS
25	PASS	PASS	PASS	74	PASS	PASS	PASS
27	PASS	PASS	PASS	75	PASS	PASS	PASS
28	PASS	PASS	PASS	77	PASS	PASS	PASS
29	PASS	PASS	PASS	78	PASS	PASS	PASS
30	PASS	PASS	PASS	79	PASS	PASS	PASS
31	PASS	PASS	PASS	80	PASS	PASS	PASS
32	PASS	PASS	PASS	81	PASS	PASS	PASS
33	PASS	PASS	PASS	82	PASS	PASS	PASS
34	PASS	PASS	PASS	83	PASS	PASS	PASS
35	PASS	PASS	PASS	84	PASS	PASS	PASS
36	PASS	PASS	PASS	88	PASS	PASS	PASS
37	PASS	PASS	PASS	89	PASS	PASS	PASS
41	PASS	PASS	PASS	90	PASS	PASS	PASS
42	PASS	PASS	PASS	91	PASS	PASS	PASS
43	PASS	PASS	PASS	92	PASS	PASS	PASS
44	PASS	PASS	PASS	93	PASS	PASS	PASS
45	PASS	PASS	PASS	94	PASS	PASS	PASS
46	PASS	PASS	PASS	95	PASS	PASS	PASS
40	PASS	PASS	PASS	96	PASS	PASS	PASS
	PASS			97			
48		PASS	PASS		PASS	PASS	PASS
49	PASS	PASS	PASS	98	PASS	PASS	PASS
50	PASS	PASS	PASS	99	PASS	PASS	PASS
51	PASS	PASS	PASS	100	PASS	PASS	PASS
52	PASS	PASS	PASS				



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VCC – VSS (+)								
Test FAIL Pin VOLTAGE	#1	#2	#3	Test FAIL Pin VOLTAGE	#1	#2	#3	
2	6000	7000	7000	60	PASS	PASS	PASS	
5	PASS	PASS	PASS	62	PASS	PASS	PASS	
13	PASS	PASS	PASS	76	PASS	PASS	PASS	
17	PASS	PASS	PASS	86	PASS	PASS	PASS	
38	PASS	PASS	PASS	87	PASS	PASS	PASS	
40	PASS	PASS	PASS					

VCC – VSS (-)								
Test FAIL Pin VOLTAGE	#1	#2	#3	Test FAIL Pin VOLTAGE	#1	#2	#3	
2	PASS	PASS	PASS	60	PASS	PASS	PASS	
5	PASS	PASS	PASS	62	PASS	PASS	PASS	
13	PASS	PASS	PASS	76	PASS	PASS	PASS	
17	PASS	PASS	PASS	86	PASS	PASS	PASS	
38	PASS	PASS	PASS	87	PASS	PASS	PASS	
40	PASS	PASS	PASS					